

FIG.1

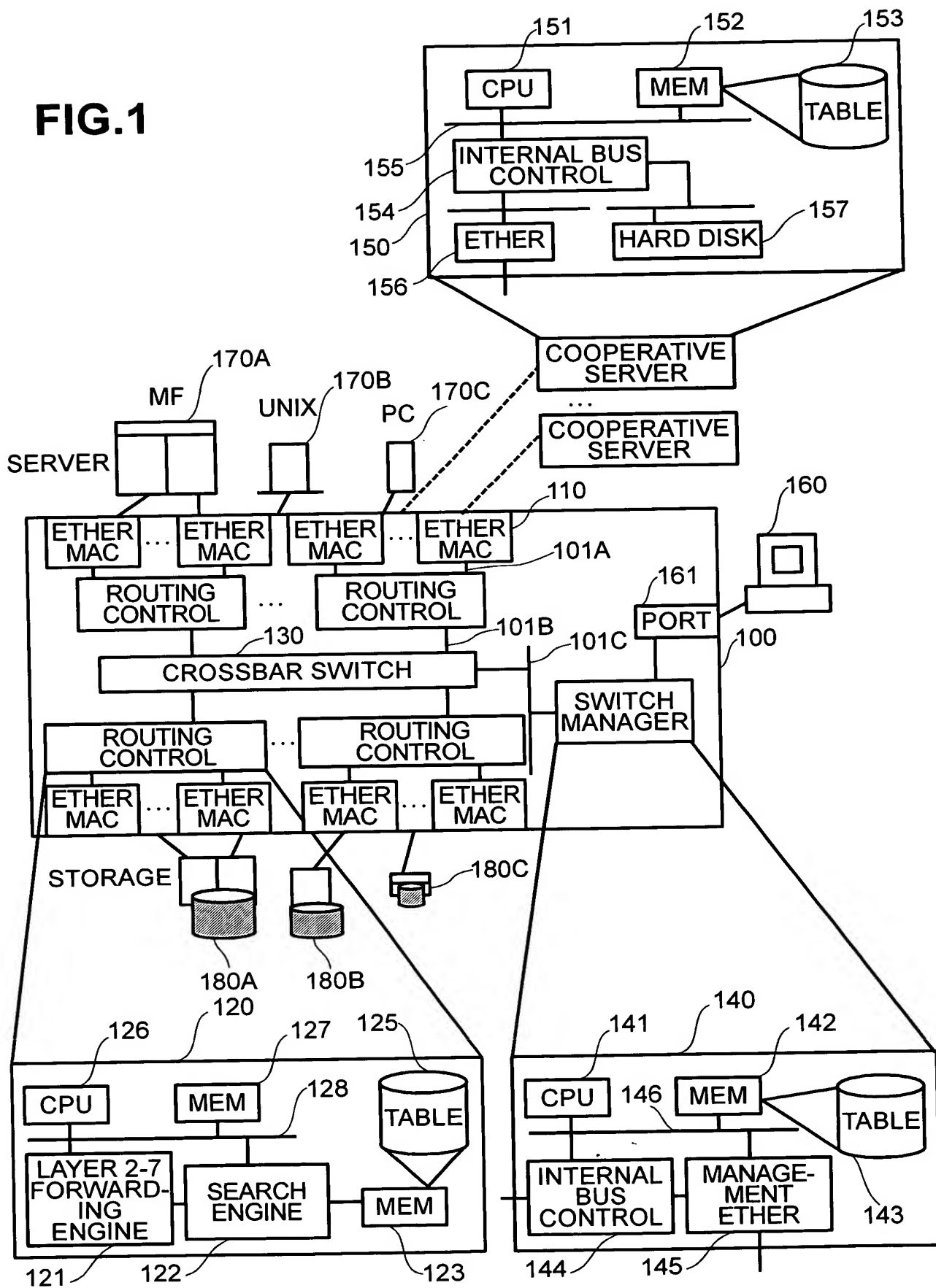


FIG.2

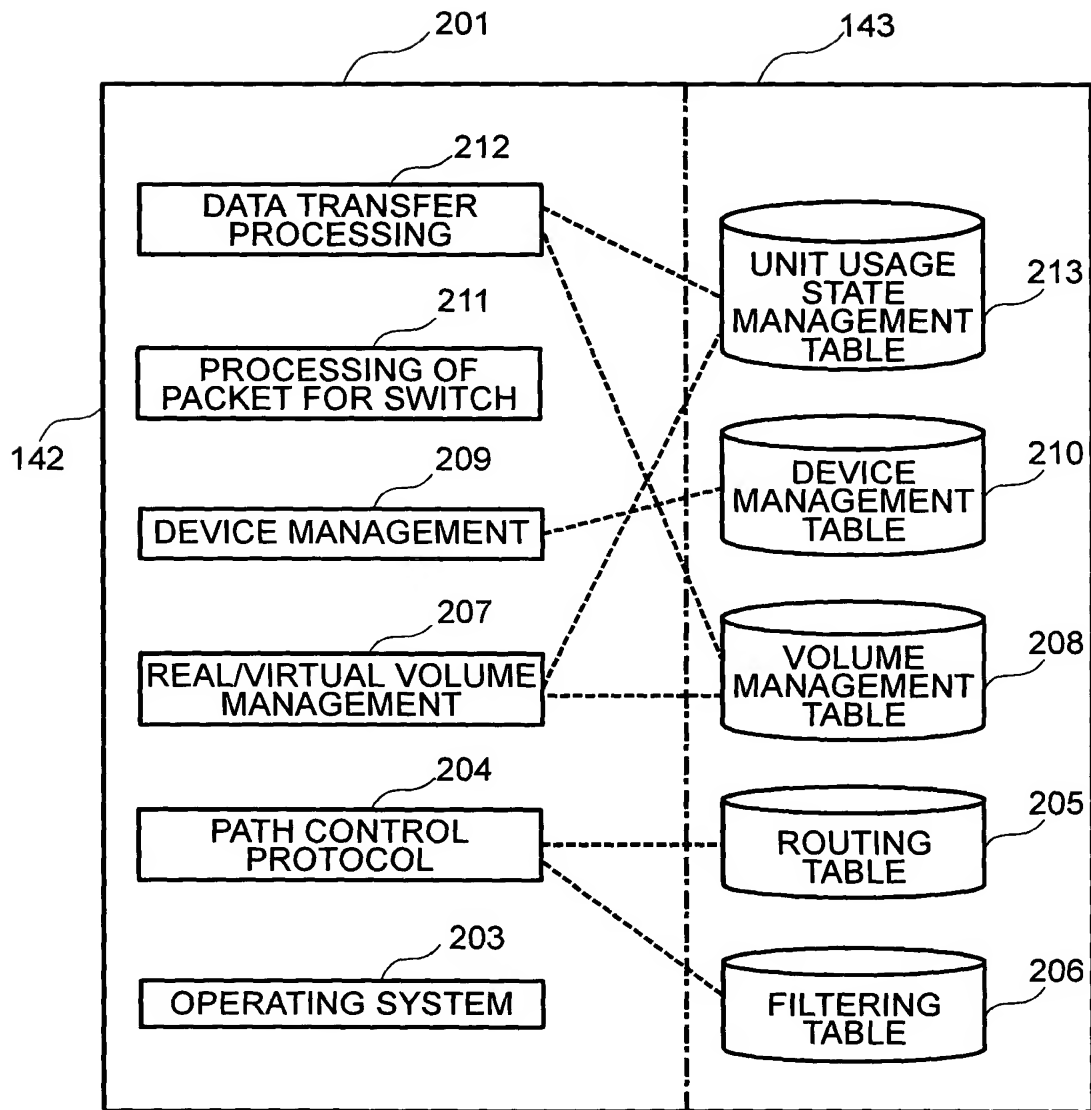


FIG.3

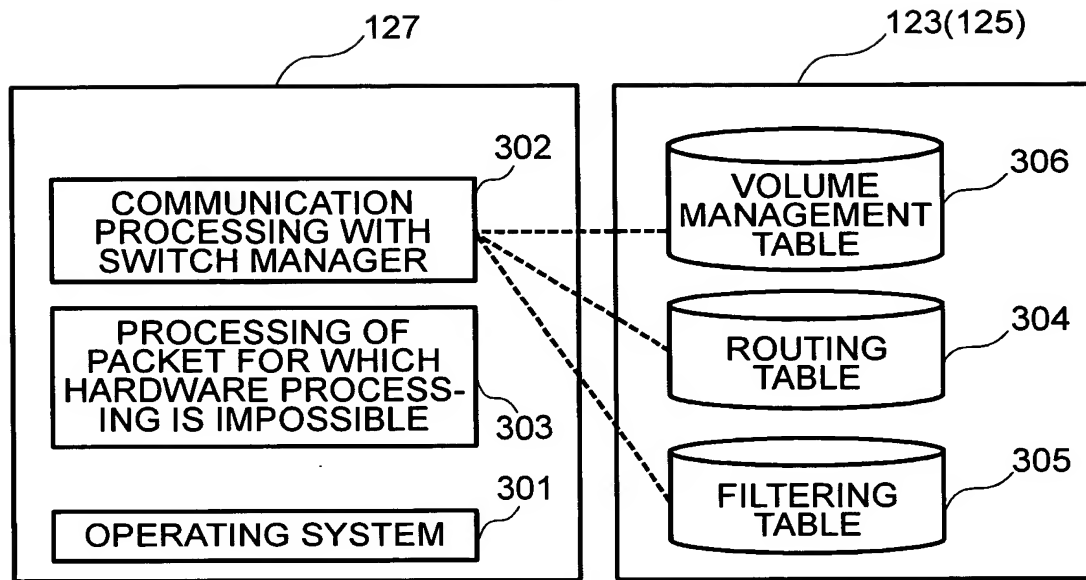


FIG.4

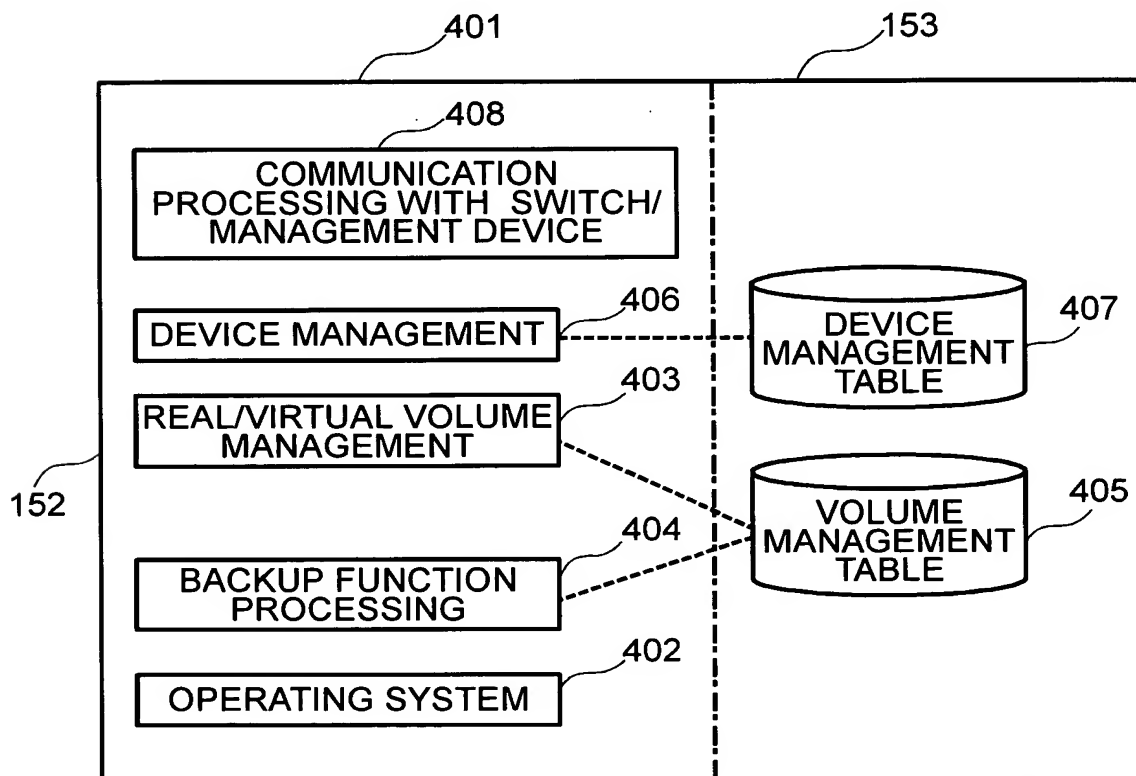


FIG.5

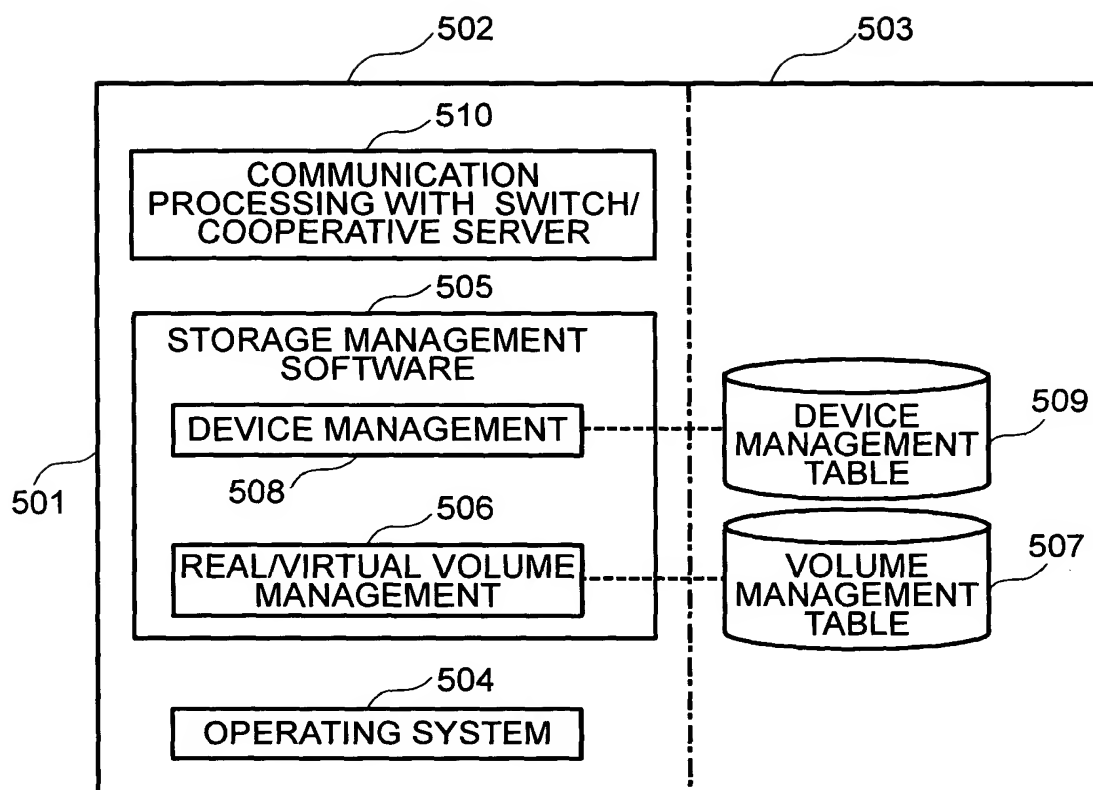


FIG.6

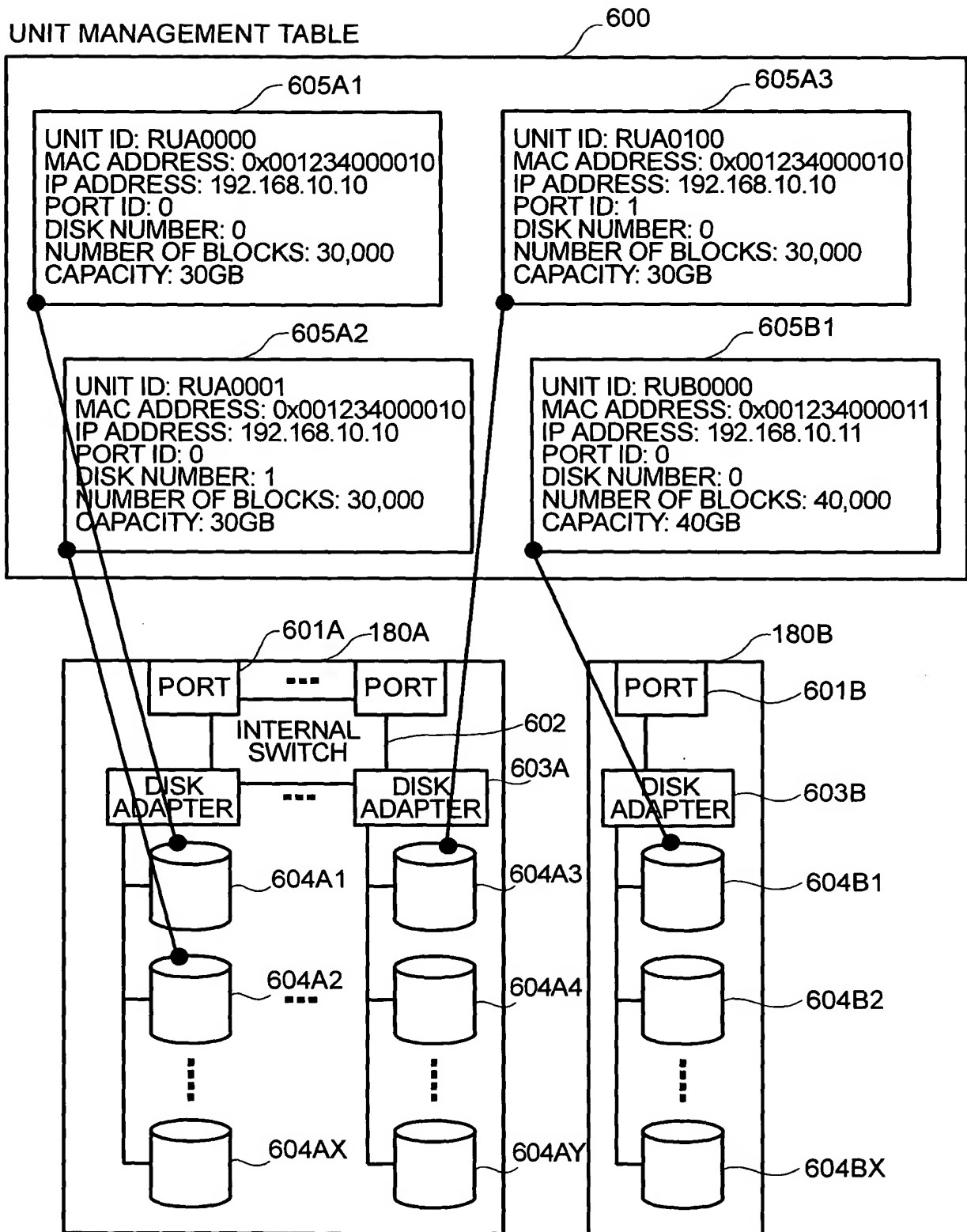


FIG.7

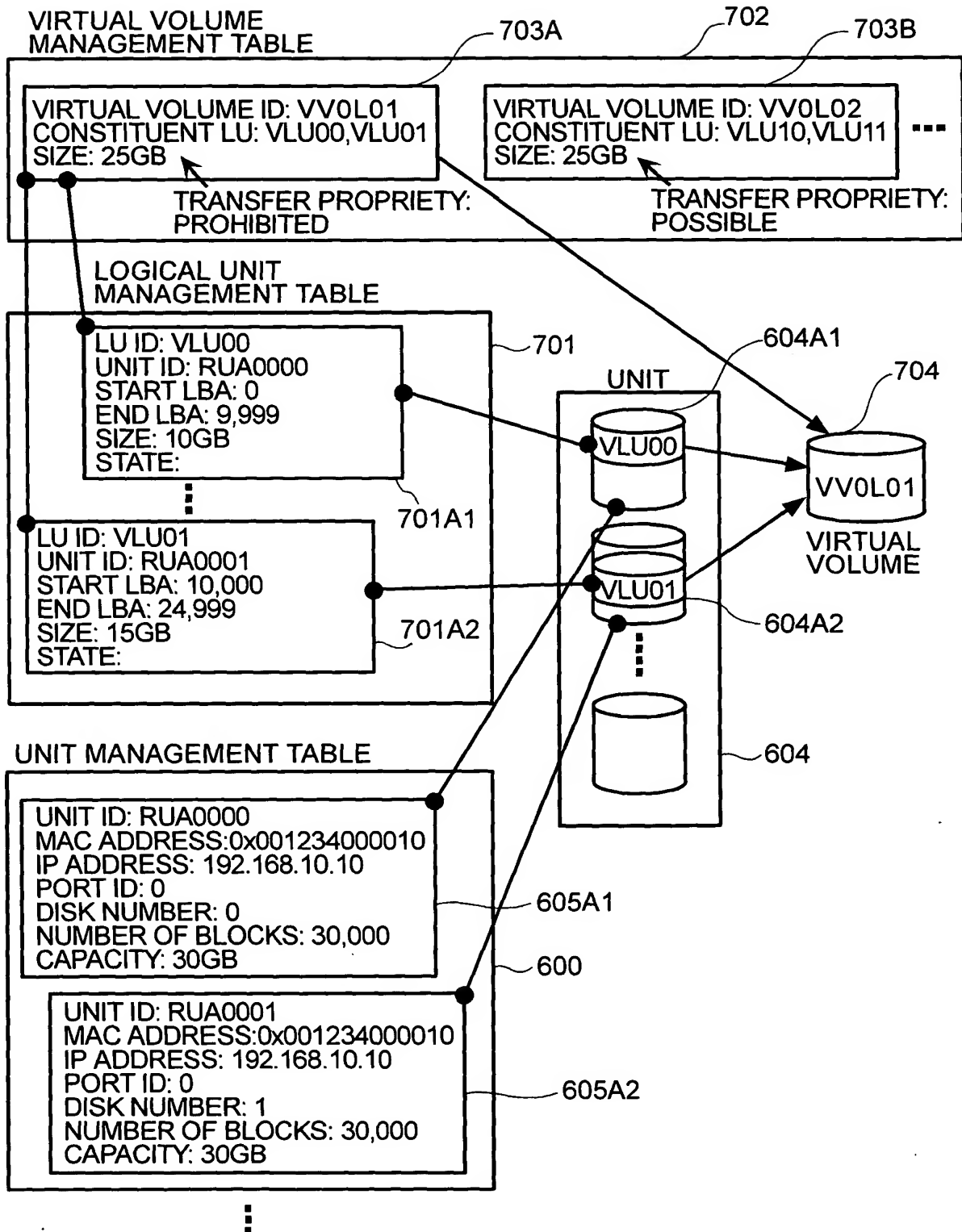


FIG.8

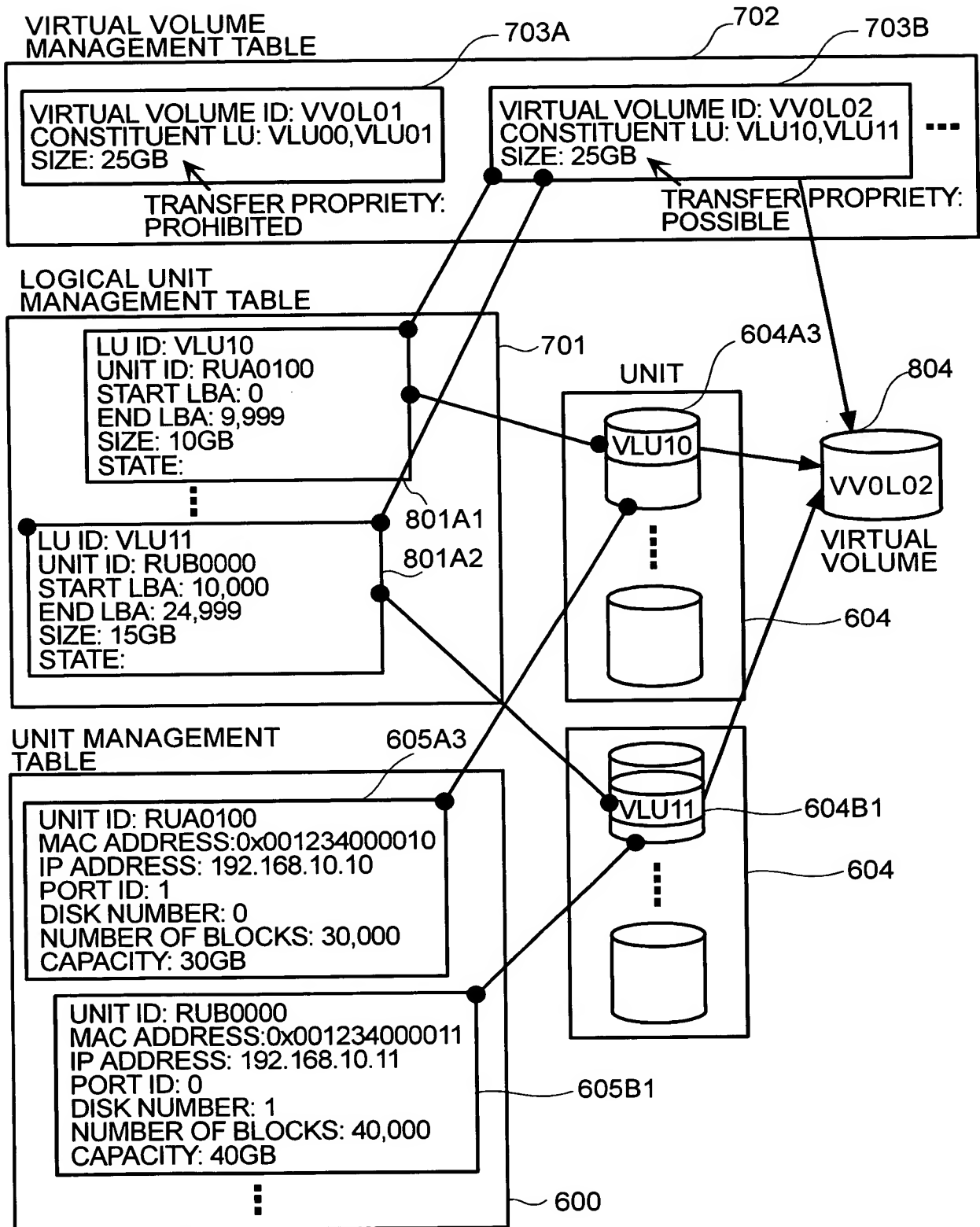


FIG.9

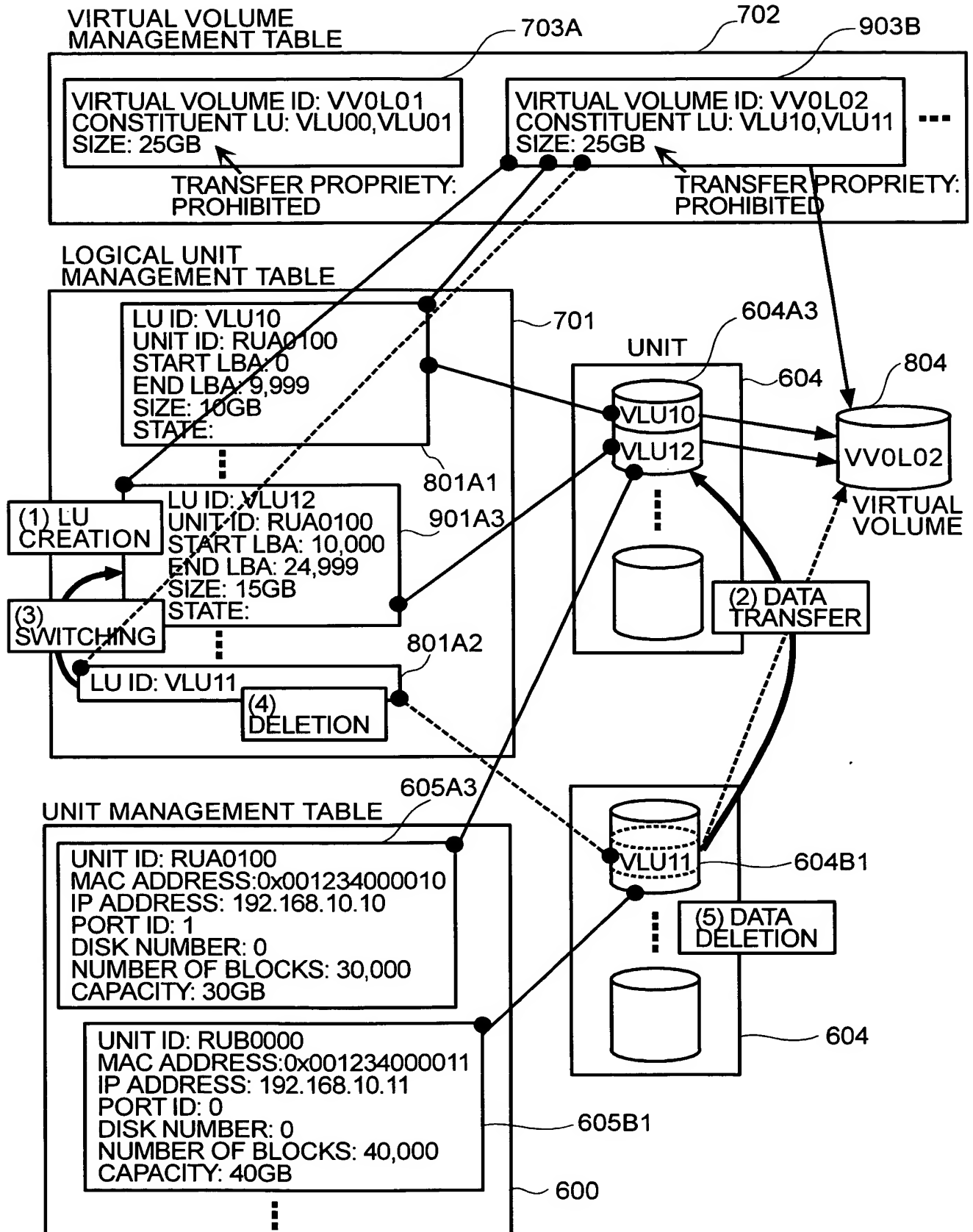


FIG.10

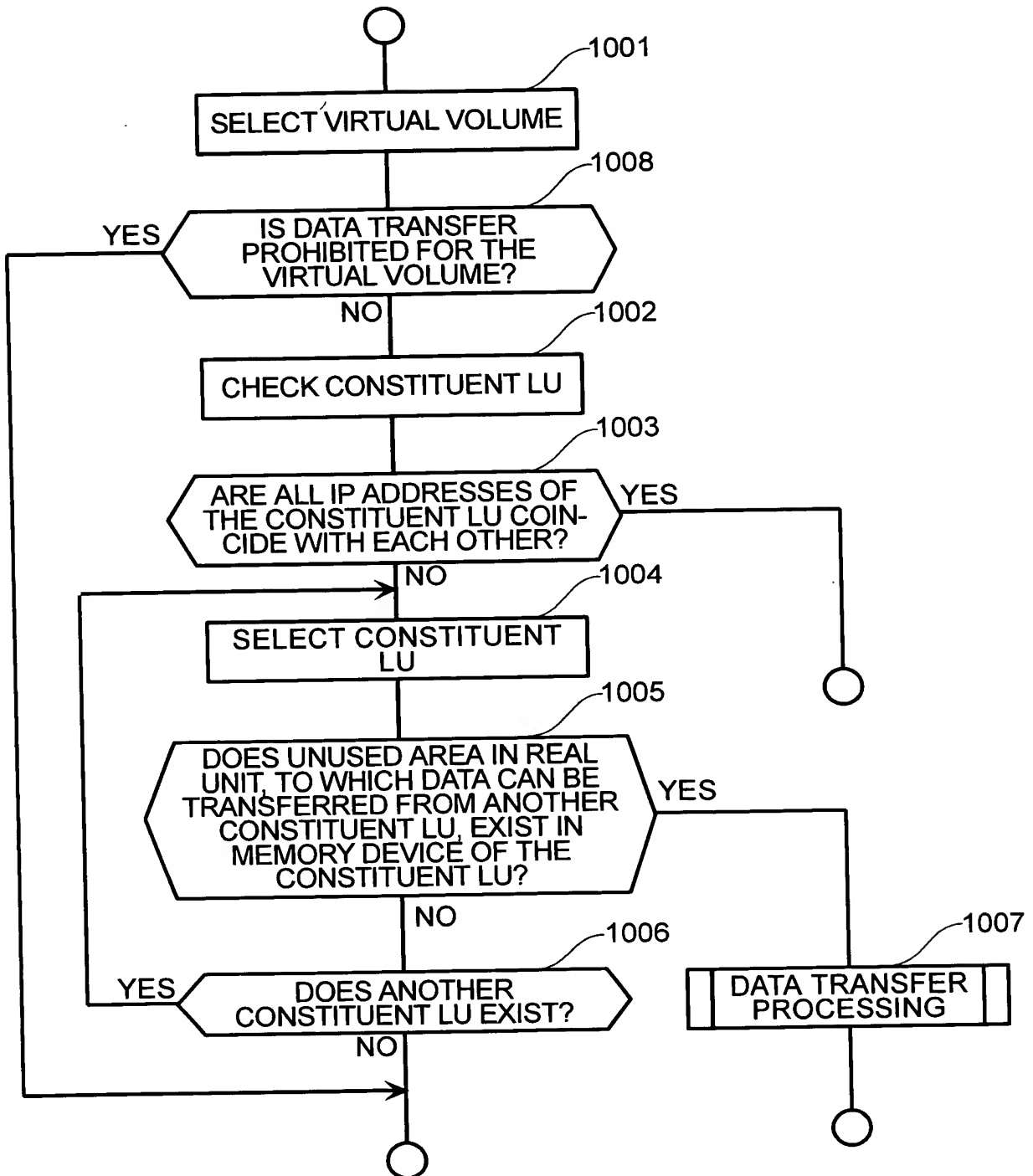


FIG.11

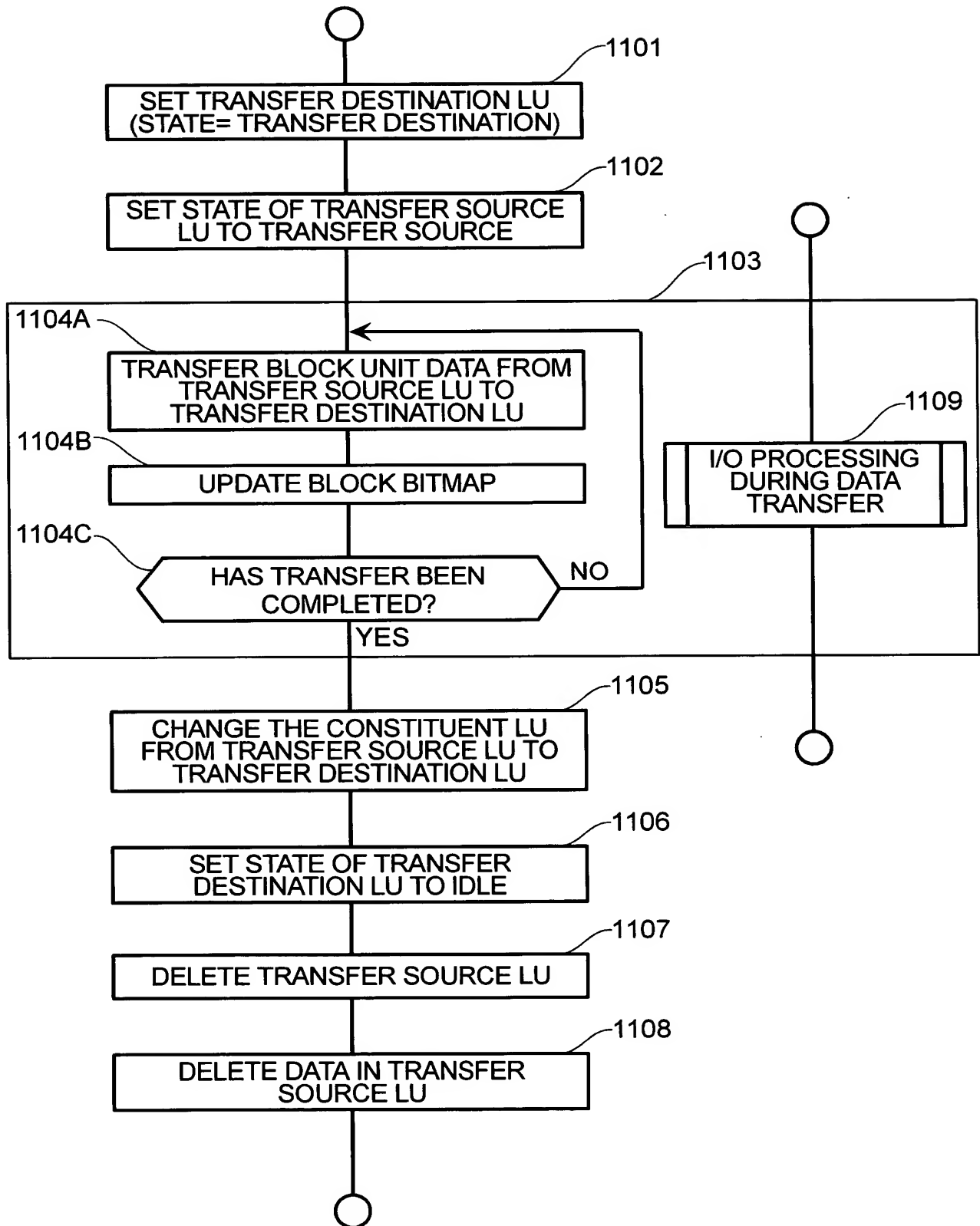


FIG.12

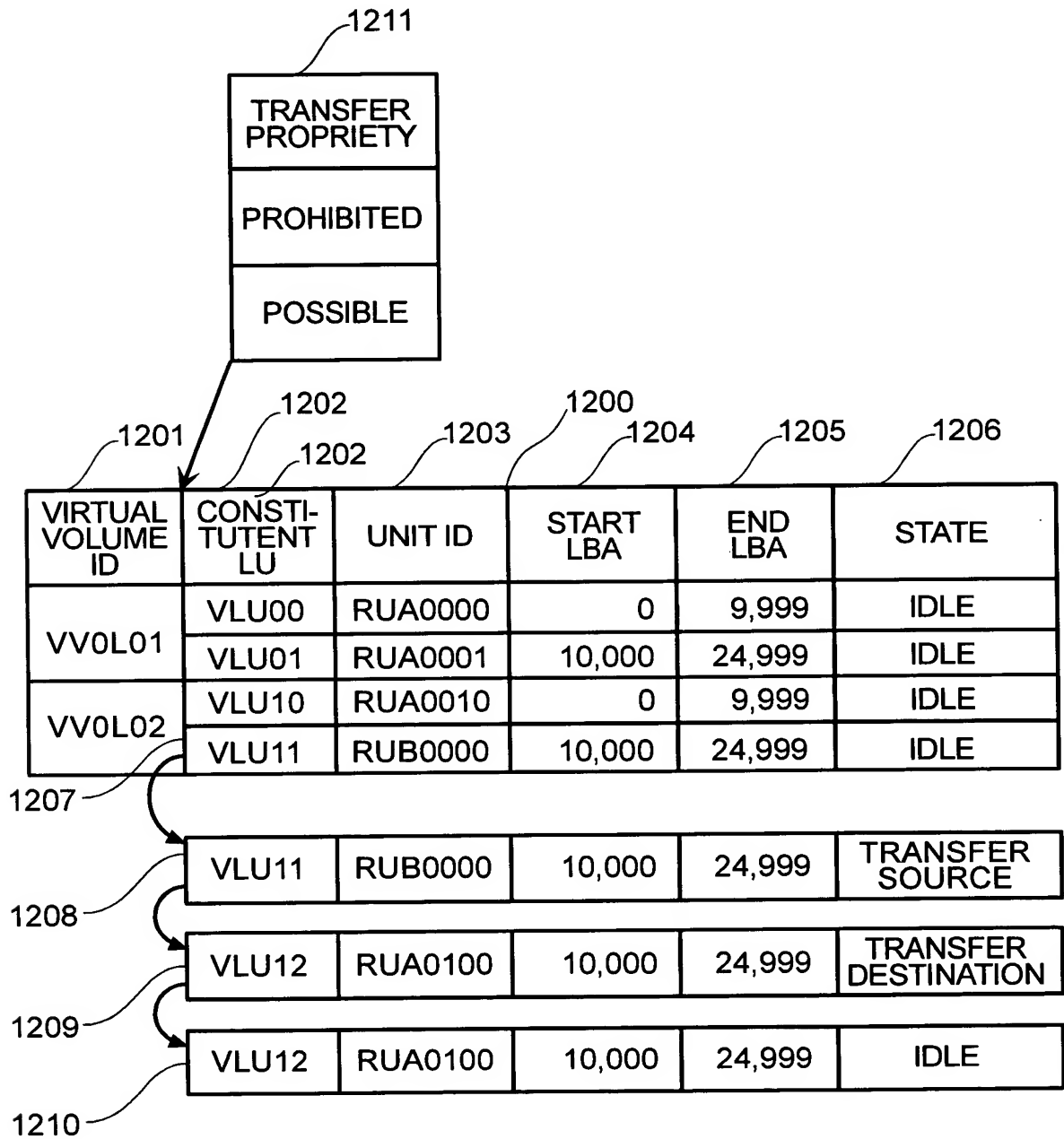
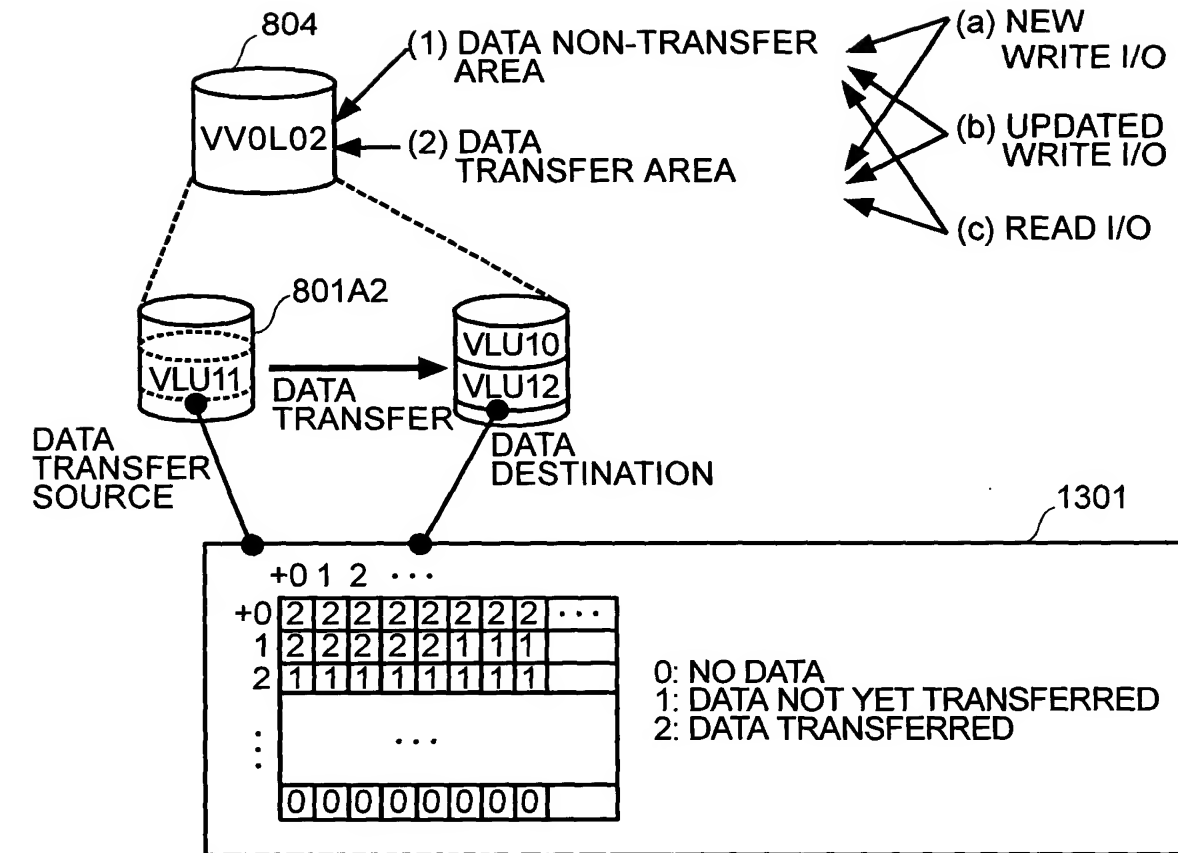


FIG.13



ACCESS DESTINATION INPUT I/O	(1) DATA NON- TRANSFER AREA	(2) DATA TRANSFER AREA	
		DATA NOT YET TRANSFERRED (PERTINENT BLOCK BITMAP= 1)	DATA TRANSFERRED (PERTINENT BLOCK BITMAP= 2)
(a) NEW WRITE	TO PERTI- NENT AREA	TO BLOCK BITMAP =0 AREA OF DATA TRANSFER DESTINATION, BITMAP AFTER WRITE= 2	
(b) UPDATED WRITE	TO PERTI- NENT AREA	• TO DATA TRANSFER SOURCE OR • DATA TRANSFER DESTINATION PERTINENT BLOCK BITMAP =2	TO DATA TRANSFER DESTINATION
(c) READ	TO PERTI- NENT AREA	TO DATA TRANSFER SOURCE	TO DATA TRANSFER DESTINATION

FIG.14

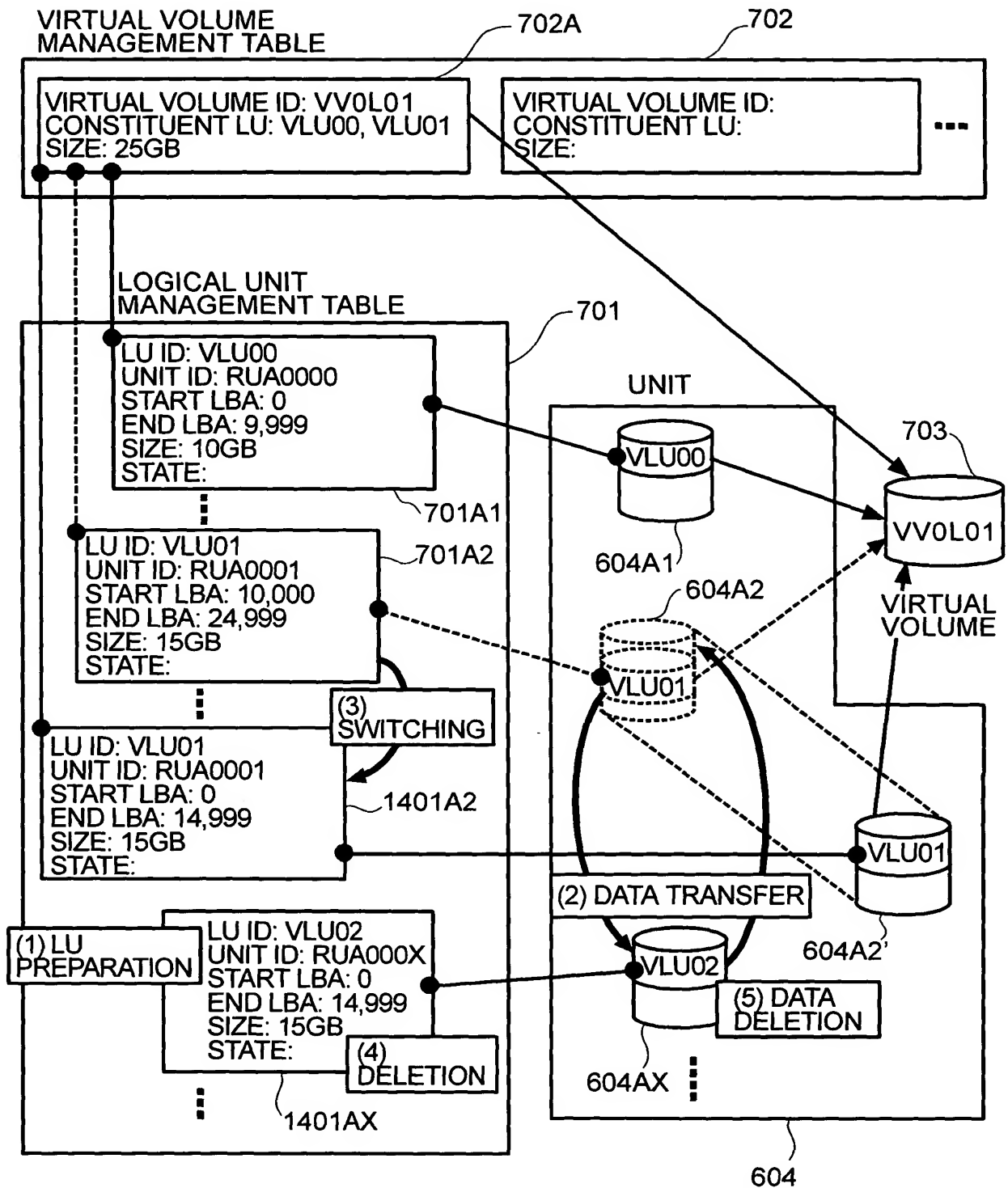


FIG.15

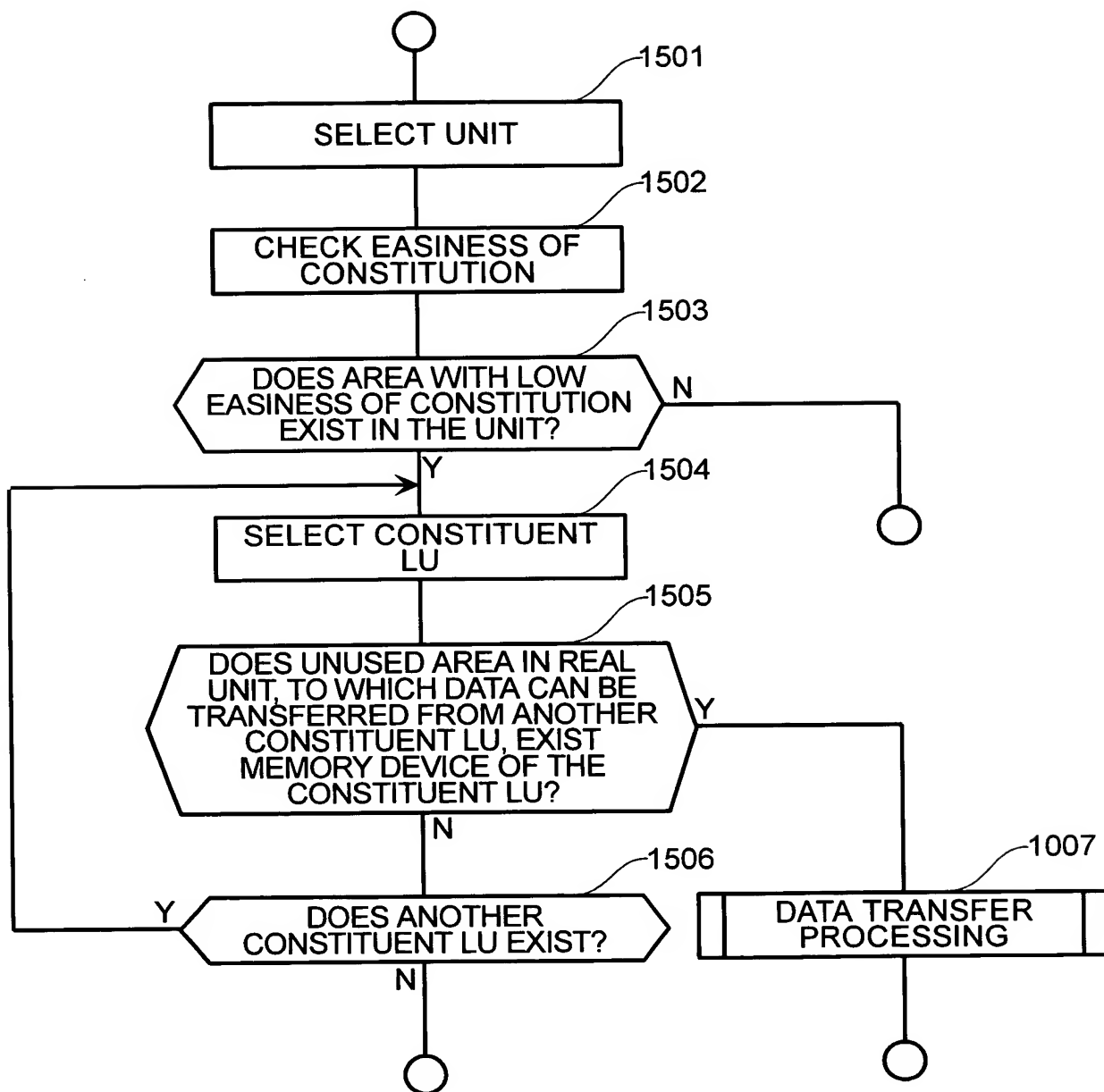


FIG.16

1601 UNIT ID	1602 START LBA	1603 END LBA	1600 1604 CONSTITUENT LU	1605 EASINESS OF CONSTITUTION
RUA0000	0	9,999	VLU00	-
	10,000	29,999		HIGH
RUA0001	0	9,999		MEDIUM
	10,000	24,999	VLU01	-
	25,000	29,999		LOW
RUA000X	0	29,999		HIGH
1607				
RUA000X	0	14,999	VLU02	-
	15,000	29,999		HIGH
1608				
RUA0001	0	14,999	VLU01	-
	15,000	29,999		HIGH
RUA000X	0	29,999		HIGH

DETERMINED ACCORDING TO
 NUMBER OF VACANT BLOCKS
 LOW: LESS THAN 7,500
 MEDIUM: LESS THAN 12,500
 HIGH: 12,500 OR MORE

FIG.17

